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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/661,793	09/12/2003	Chi-An Kao	TS01-1037	8353
8933	7590 04/30/2007 DIC LLD		EXAM	INER
DUANE MOR IP DEPARTM	-	NGUYEN, KHIEM D		
30 SOUTH 17'	TH STREET IIA, PA 19103-4196		, ART UNIT	PAPER NUMBER
			2823	-
			MAIL DATE	DELIVERY MODE
			. 04/30/2007	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

		Application No.	Applicant(s)			
Office Action Summany						
		10/661,793	KAO ET AL.			
	Office Action Summary	Examiner	Art Unit			
		Khiem D. Nguyen	2823			
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply						
WHIC - Exte after - If NC - Failu Any	ORTENED STATUTORY PERIOD FOR REPLY CHEVER IS LONGER, FROM THE MAILING DATE of time may be available under the provisions of 37 CFR 1.13 SIX (6) MONTHS from the mailing date of this communication. O period for reply is specified above, the maximum statutory period we are to reply within the set or extended period for reply will, by statute, reply received by the Office later than three months after the mailing ed patent term adjustment. See 37 CFR 1.704(b).	ATE OF THIS COMN 66(a). In no event, however, vill apply and will expire SIX (or cause the application to become	MUNICATION. may a reply be timely filed by MONTHS from the mailing date of this communication. come ABANDONED (35 U.S.C. § 133).			
Status	·		•			
1)⊠	Responsive to communication(s) filed on <u>05 February 2007</u> .					
2a)□	This action is FINAL. 2b)⊠ This action is non-final.					
3)	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is					
	closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.					
Disposit	ion of Claims					
4)⊠	Claim(s) <u>8-17</u> is/are pending in the application.					
	4a) Of the above claim(s) is/are withdrawn from consideration.					
5)⊠	5)⊠ Claim(s) <u>12-14</u> is/are allowed.					
6)🖂	Claim(s) <u>8-11 and 15-17</u> is/are rejected.					
7)	Claim(s) is/are objected to.					
8)□	Claim(s) are subject to restriction and/or	election requiremer	ıt.			
Applicat	ion Papers		·			
9)	The specification is objected to by the Examiner	r.				
10)⊠ The drawing(s) filed on <u>12 September 2003</u> is/are: a)⊠ accepted or b)□ objected to by the Examiner.						
·	Applicant may not request that any objection to the o		•			
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).						
11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.						
Priority (under 35 U.S.C. § 119					
12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).						
a) ☐ All b) ☐ Some * c) ☐ None of:						
1. Certified copies of the priority documents have been received.						
2. Certified copies of the priority documents have been received in Application No						
3. Copies of the certified copies of the priority documents have been received in this National Stage						
application from the International Bureau (PCT Rule 17.2(a)).						
* See the attached detailed Office action for a list of the certified copies not received.						
Attachmen	t(s)		••			
1) Notice of References Cited (PTO-892) 4) Interview Summary (PTO-413)						
2) Notice of Draftsperson's Patent Drawing Review (PTO-948) Paper No(s)/Mail Date						
	3) Information Disclosure Statement(s) (PTO/SB/08) Paper No(s)/Mail Date 5) Notice of Informal Patent Application 6) Other:					
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DETAILED ACTION

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Continued Examination Under 37 CFR 1.114

A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after final rejection. Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, the finality of the previous Office action has been withdrawn pursuant to 37 CFR 1.114. Applicants' submission filed on December 05th, 2006 has been entered. A new rejection base on the newly discovered reference to Mui et al. (U.S. Patent 6,924,088) is made as set forth in this Office Action. Claims (8-17) are pending in the application.

Claim Rejections - 35 USC § 102

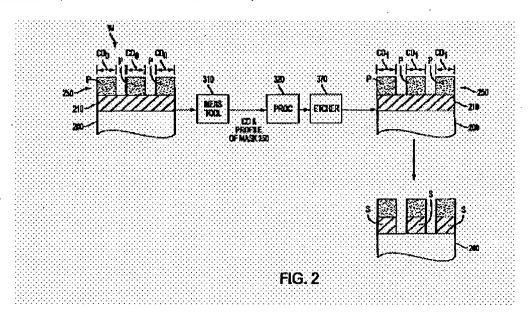
2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.
- 3. Claims 8-11 and 15-17 are rejected under 35 U.S.C. 102(e) as being anticipated by Mui et al. (U.S. Patent 6,924,088).

In re claim 8, <u>Mui</u> discloses a system for creation of an opening of controllable format through a layer of insulation material, comprising:

means 370 for creating an opening (via patterns P, col. 6, lines 29-30) through a layer of etch resist material 250 (col. 6, line 29) provided over the surface of a layer of insulating material (col. 6, lines 31-37) having been deposited over the surface of a substrate 200 (col. 6, lines 23-51 and FIG. 2);



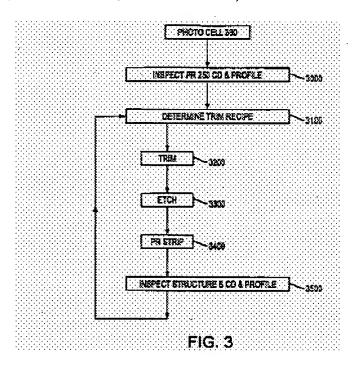
means for measuring 310 an obtained critical dimension measurement of the opening created through the layer of etch resist material 250 (col. 6, lines 40-41 and FIG. 2);

means, including a feedback mechanism (col. 9, lines 11-24), for assuring that the obtained critical dimension measurement of the opening created through the layer of etch resist material 250 is within design specification, the feedback mechanism communicating with the means for creating an opening through a layer or etch resist material 250 to control the critical dimension (CD) measurement of the opening (col. 9, lines 11-44);

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means 370 for creating an opening through the layer of insulation material, whereby a diameter of the layer of insulation material is dependent on a diameter of the opening (via patterns P, col. 6, lines 29-30) created through the layer of etch resist material 250 (col. 6, lines 34-51); and

means, including a feedback mechanism, (col. 9, lines 5-24) for assuring that the opening created through the layer of insulation material is within design specification (col. 8, line 58 to col. 9, line 4 and FIG. 3).



In re claim 9, as applied to claim 8 above, <u>Mui</u> discloses all claimed limitations including the limitation wherein means, including a feedback mechanism (col. 9, lines 5-24), for assuring that an obtained critical dimension measurement of the opening (via patterns P, col. 6, lines 29-30) created through the layer of etch resist material 250 is within design specification comprising: means for linking to a software supervisory function 320, thereby including data transmission functions (col. 9, lines 13-17), means

for linking to a software function equally being linked to a software supervisory function, thereby including data transmission functions; means for data manipulating capabilities, thereby including manipulating interdependent data; means for interfacing with semiconductor equipment, thereby including equipment functioning in a supporting role to the semiconductor equipment; and means for creating instructions for the semiconductor equipment, thereby including equipment functioning in a supporting role to the semiconductor equipment (col. 9, lines 5-24).

In re claim 10, as applied to claim 8 above, <u>Mui</u> discloses all claimed limitations including the limitation wherein means for assuring that the opening created through the layer of insulation material is within design specification comprising: means for linking to a software supervisory function 320 (col. 9, lines 13-15), thereby including data transmission functions, means for linking to a software function equally being linked to a software supervisory function, thereby including data transmission functions; means for data manipulating capabilities, thereby including manipulating interdependent data; means for interfacing with semiconductor equipment, thereby including equipment functioning in a supporting role to the semiconductor equipment; and means for creating instructions for the semiconductor equipment, thereby including equipment functioning in a supporting role to the semiconductor equipment (col. 9, lines 5-24).

In re claim 11, as applied to claim 8 above, <u>Mui</u> discloses all claimed limitations including the limitation wherein the system further comprising means for creating an opening (via patterns P, col. 6, lines 29-30) having non-linear sidewalls through a layer of

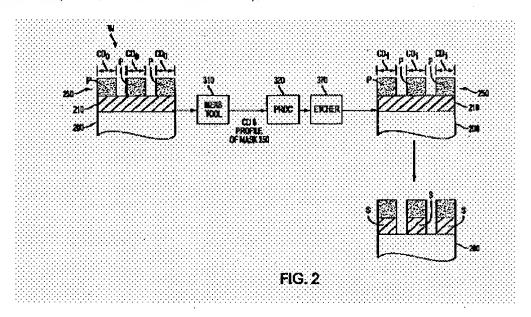
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insulation material by applying a high-polymer based etch to the surface of the layer of insulation material (col. 6, lines 23-51).

In re claim 15, <u>Mui</u> discloses a system for creation of an opening of controllable format through a layer of insulation material, comprising:

means 370 for creating an opening (via patterns P, col. 6, lines 29-30) through a layer of etch resist material 250 (col. 6, line 29) provided over the surface of a layer of insulating material (col. 6, lines 31-37) having been deposited over the surface of a substrate 200 (col. 6, lines 23-51 and FIG. 2);



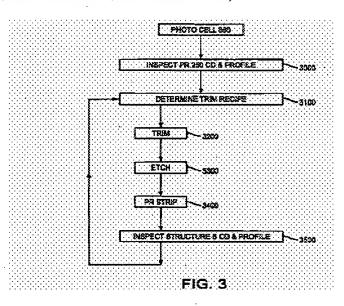
means, including a feedback mechanism (col. 9, lines 11-24), for obtaining a critical dimension measurement of the opening created through the layer of etch resist material 250 assuring that the critical dimension measurement (CD) is within design specification, the feedback mechanism communicating with the means for creating an opening through a layer of etch resist material 250 to control the critical dimension measurement (CD) of the opening (col. 9, lines 11-44);

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means 370 for creating an opening having non-linear sidewalls through the layer of insulation material by applying a high-polymer based etch to the surface of the layer of insulation material, whereby a diameter of opening having non-linear sidewalls is dependent on a diameter of the opening (via patterns P, col. 6, lines 29-30) created through the layer of etch resist material 250 (col. 6, lines 34-51); and

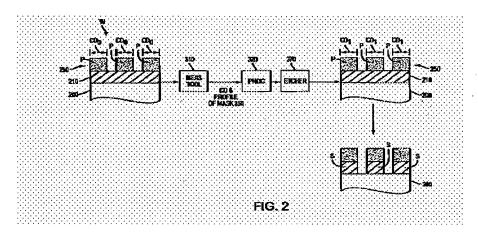
means, including a feedback mechanism, (col. 9, lines 5-24) for assuring that the opening created through the layer of insulation material is within design specification (col. 8, line 58 to col. 9, line 4 and FIG. 3).



In re claim 16, <u>Mui</u> discloses a system for creation of an opening of controllable format through a layer of insulation material, comprising:

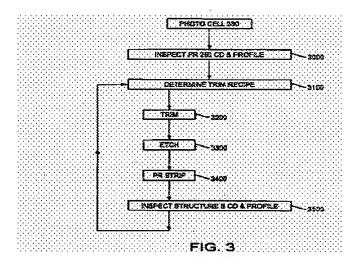
means, including a feedback mechanism (col. 9, lines 11-24), for creating an opening (via patterns P, col. 6, lines 29-30) through a layer of etch resist material 250 provided over the surface of a layer of insulating material (col. 6, lines 31-37) having been deposited over the surface of a substrate 200, such that the opening has a critical

dimension measurement (CD) that is within design specification; (col. 9, lines 11-44 and FIG. 2);



means 370 for creating an opening through the layer of insulation material, whereby a diameter of layer of insulation material is dependent on a diameter of the opening (via patterns P, col. 6, lines 29-30) created through the layer of etch resist material 250 (col. 6, lines 34-51); and

means, including a feedback mechanism, (col. 9, lines 5-24) for assuring that the opening created through the layer of insulation material is within design specification (col. 8, line 58 to col. 9, line 4 and FIG. 3).



In re claim 17, as applied to claim 16 above, <u>Mui</u> discloses all claimed limitations including the limitation wherein the means, including a feedback mechanism (col. 9, lines 11-24), for creating an opening (via patterns P, col. 6, lines 29-30) include means for making corrections to an original critical dimension measurement (CD) that is not within design specification (col. 9, lines 13-24).

Allowable Subject Matter

4. Claims 12-14 were previously allowed over prior art of record.

Response to Applicants' Amendment and Argument

5. Applicants' arguments with respect to claims 8-11 and 15-17 have been considered but are most in view of the new ground(s) of rejection.

Conclusion

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Khiem D. Nguyen whose telephone number is (571) 272-1865. The examiner can normally be reached on Monday-Friday (8:30 AM - 5:30 PM).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Matthew S. Smith can be reached on (571) 272-1907. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

K.N. April 25, 2007

> BROOK KEBEDE PRIMARY EXAMINER

Brook Kekede